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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

NONE CL

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

NONE CL

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 05/27/2004

Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	<u>Cheng Ku</u> CL Examiner's Signature Initials	TAIWAN	5	27	2

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## TITLE

Method of forming polysilicon gate structures with specific edge profiles for optimization of LDD offset spacing

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